Applicant(s)/Patent Under Application/Control No. Reexamination 10/617,487 SALAMA ET AL Notice of References Cited Art Unit Examiner Page 1 of 1 2822 Monica Lewis **U.S. PATENT DOCUMENTS** Document Number Country Code-Number-Kind Code Date Classification Name MM-YYYY 257/500 US-2003/0222327 12-2003 Yamaguchi et al. Α 438/156 US-2003/0219933 11-2003 Yamauchi et al. В 257/492 11-2003 Parthasarathy et al. US-2003/0214009 C 257/491 10-2004 Onishi et al. US-200402112032 D 257/342 06-2004 Onishi et al. US-6,756,636 Ε 257/335 US-20040232483 11-2004 Kitagawa et al. 11-2004 438/268 Disney et al. US-6,815,293 G Kawaguchi et al. 257/341 US-6,297,534 10-2001 257/335 08-2004 Kitagawa et al. US-6,777,746 US-J US-Κ US-US-М FOREIGN PATENT DOCUMENTS Date Document Number * Classification Name Country Country Code-Number-Kind Code MM-YYYY N 0 Р Q R s T **NON-PATENT DOCUMENTS** Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

W

Sheet 1 of 1								
TOTAL TON DISCLOSURE CITATION				Docter Number (Optional) SALA:003 SERIAL NO.: 10/617,4		IAL NO.: 10/617,487	' 	
(Use several sheets if necessary)				APPLICANT(1) SALAMA et al.				
FEB 0 3 2004				FILING DATE: July 11, 2003		Group An Unit 28	Group An Unit 2811	
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
MC	US	4,754,310	6/88	Coe	357	13		
MU	US	5,216,275	6/93	Chen	257	493		
MY.	US	5,438,215	8/95	Tihanyi	257	401		
M	us	2003/0190789	10/03	Salama et al.	438	286		
OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)								
		MC	"High Voltage Thin Layer Devices (Resurf Devices)"; J.A. APPELS et al.; IEEE International Electron Device Meeting (IEDM); Dig. Tech Papers; pp. 238-241; 1979.					
		MC	"Theory of a novel voltage-sustaining layer for power devices"; X.B. CHEN et al.; Microelectronics Journal, Vol. 29; pp. 1005-1011; 1998.					
		MC	"COOLMOSTM - a new milestone in high voltage Power MOS"; L. LORENZ et al.; Proceedings of the 11th International Symposium on Power Semiconductor Devices and ICs (ISPSD); pp. 3-10; 1999.					
		mU	"Super Junction LDMOST in Silicon-On-Sapphire Technology (SJ-LDMOST); Sameh NASSIF-KHALIL et al.; International Symposium on Power Semiconductor Devices and ICs (ISPSD), Proceedings; pp. 81-84; 2002.					
		mc	"170V Super Junction - LDMOST in a 0.5 µm Commercial CMOS/SOS Technology"; S.G. NASSIF-KHALIL et al.; International Symposium on Power Semiconductor Devices and ICs (ISPSD), Proceedings, accepted for publication; 4 pages. "Extended (180V) Voltage in 0.6 µm Thin-Layer-SOI A-BCD3 Technology on 1 µm BOX for Display, Automotive & Consumer Applications"; A.W. LUDIKHUIZE et al.; International Symposium on Power Semiconductor Devices and ICs (ISPSD), Proceedings; pp. 77-80; 2002.					
		MU						
		mc	"A Versatile 700-1200-V IC Process for Analog and Switching Applications", Adriaan LUDIKHUIZE; IEEE Transactions on Electron Devices, vol. 38; pp. 1582-1589; 1991.					
		MC	"Experimental Results and Simulation Analysis of 250V Super Trench Power MOSFET (STM); T. NITTA et al.; International Symposium on Power Semiconductor Devices and ICs (ISPSD), Proceedings; pp. 77-80; 2000.					
		m	"Super-Junction LDMOST on a Silicon-on-Sapphire Substrate"; S. NASSIF-KHALIL et al.; IEEE Transactions on Electron Devices, Vol. 50, No. 5; May 2003; pp. 1385-1391.					
EXAMINER			DATE CONSIDERED 12/0					

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.